

416 Rec'd PCT PTO 15 APR 1999

PCT
#5/ approved
Declaration
10/14/99
VJ



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Kohei TATSUMI, et al.
Serial No. : 09/254,118
Filed : February 25, 1999
Int'l. Appln. No. : PCT/JP97/02988
Filed : August 27, 1997
For : METHOD OF PARTIALLY PLATING SUBSTRATE
FOR ELECTRONIC DEVICES

Assistant Commissioner for
Patents
Washington, D.C. 20231

SUBMISSION OF ORIGINAL DECLARATION

S I R :

Applicants submit herewith an original executed Declaration and Power of Attorney, identified by the international application serial number and filing date, in the above-identified application.

The Office is hereby authorized to charge any fees required by this paper to Deposit Account No. 11-0600. This paper is filed in duplicate.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner of Patents and Trademarks, Washington, D.C. 20231, on

Date April 12, 1999 Atty's Reg. # 18,918

Atty's Signature Edward W. Greason
KENYON & KENYON
EDWARD W. GREASON

One Broadway
New York, N.Y. 10004
(212) 425-7200

Dated: April 12, 1999
L:\NY2DOCS\EWG\PU01\76523-1

Respectfully submitted,

KENYON & KENYON

By Edward W. Greason
Edward W. Greason
Reg. No. 18, 918

RECEIVED
SEP 15 1999
TECHNOLOGY CENTER 2800